

STATUS OF THE CLAIMS

Claims 1-20 are pending.

Claims 13-16 were allowed.

Claims 1, 17-20 were rejected.

Claims 2-12 were objected to.

By this response, please **amend claim 1, 3, 6, 7, 9, 10, 11, 17, and delete claim 2.**

REMARKS

The Examiner rejected claims 1, 17-20 were rejected under 35 USC 103(a) as being patentable over Paff (US Patent No. 5,553,064) in view of Decuir (US Patent No. 5,781,028).

Claim 1 has been amended to include the features of old claim 2. More specifically, claim 1 has been amended to include:

“wherein:

the data pair comprises a first and second data line;

the return data pair comprises a first and second return data line;

the transmitter transmits the first video data stream by switching a DC-balanced current between the first and second data line, thus creating a first and second AC current on the first and second data line;

the first and second summing resistor convert the current transmitted across the data pair to a DC return current by merging the first AC current with the second AC current; and

the return transmitter transmits the return video data stream by switching the DC return current between the first and second return data line, thus creating a first and second AC return current on the first and second return data line.”

None of the cited references disclose "a return data pair comprises a first and second return line; and the return transmitter transmits the return video data stream by switching the DC return between the first and second return data line, thus creating a first and second AC return current on the first and second return data line."

Claims 1 is patentable over the cited references. Claim 2 has been deleted. Claims 3-12 are directly or indirectly dependent on claim 1. Therefore, claims 3-12 are patentable over the cited references.

Amended claim 17 includes the features:

transmitting the serial video data signal across a first and second data line by alternately transferring the DC input current between a first data line and a second data line to yield a first and second AC current, the first and second currents alternating between zero and a fixed value, the first and second currents one hundred eighty degrees out of phase with one another;

and

transmitting the return serial video data signal across a first and second return data line by alternating the DC return current across the first and second return data lines to yield a first and second return AC current, the first and second return AC currents alternating between zero and a fixed value, the first and second return AC currents one hundred eighty degrees out of phase with one another;

None of the cited references include this combination of features. Therefore, amended claim 17 is patentable over the cited references.

Claims 18-20 are directly or indirectly dependent on claim 17. Therefore, claims 18-20 are patentable over the cited references.

No new matter has been added by these amendemts.

Applicants respectfully suggest that each of the claims presently in the application are distinct over the prior art and that the application is now in condition for allowance. Accordingly, Applicants request that the rejections be withdrawn and the claims be allowed.

Respectfully submitted

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